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Application ID:

10782648

Title of Invention:

POST AMPLIFIER ARRAY

INTEGRATED CIRCUIT

First Named Inventor:

**Nelson DIAZ** 

Domestic/Foreign Application:

**Domestic Application** 

Filing Date:

2004-02-19

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Submission Type:

Information Disclosure

Statement

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4118

**Attorney Docket Number:** 

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## **TRANSMITTAL**

Electronic Version v1.1 Stylesheet Version v1.1.0

Title of Invention

POST AMPLIFIER ARRAY INTEGRATED CIRCUIT

**Application Number:** 

10/782648

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Submitted by:	Elec. Sign.	Sign. Capacity
Mark E. Tetreault Registered Number: 48289	/mark e. tetreault/	Attorney

Documents being submitted

Files

us-ids

P0951-US-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



## REPORT OF THE PROPERTY OF THE

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

POST AMPLIFIER ARRAY INTEGRATED CIRCUIT

Application Number:

10/782648

Confirmation Number:

4118

First Named Applicant: Nelson DIAZ
Attorney Docket Number: O013P00951US

Art Unit:

2817

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( 4366468 or 4388732 or 4594560 or 4989003 or 5257285 or 5281805 or 5381052 or 5381146 or 5442321 or 5528692 or 5594576 or 5634202 or 5650608 or 5875126 or 5973313 or 6091300

or 6023191 or 6041084 ).pn.

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4366468	1982-12-28	Yoneyama		340	347 NT
	2	4388732	1983-06-14	Hansel		455	608
	3	4594560	1986-06-10	Dingwall et al.		330	296
	4	4989003	1991-01-29	Sauer		341	136
	5	5257285	1993-10-26	Thorp		375	11
	6	5281805	1994-01-25	Sauer		250	214
	7	5381052	1995-01-10	Kolte		327	60
	8	5381146	1995-01-10	Kolte		341	132
	9	5442321	1995-08-15	Bayruns et al.		330	282
	10	5528692	1996-06-18	Hester et al.		380	38
	11	5594576	1997-01-14	Sutherland et al.		359	118
	12	5634202	1997-05-27	Connell et al.		455	110
	13	5650608	1997-07-22	Redford et al.		250	210
	14	5875126	1999-02-23	Minch et al.		365	185.01
	15	5973313	1999-10-26	Redford et al.		250	210
	16	6091300	2000-07-18	Setty et al.		330	258

